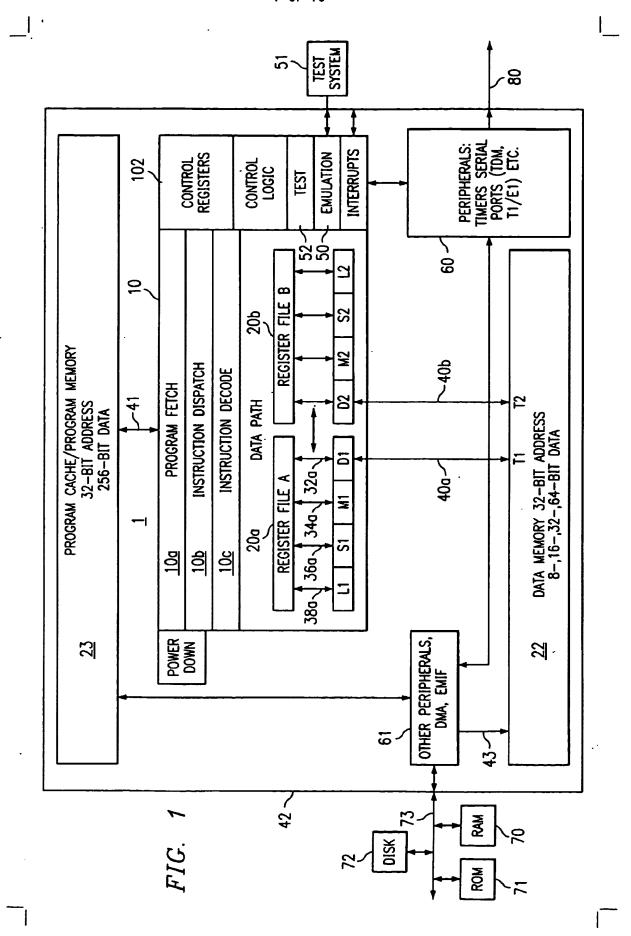
## Amendment to the Drawings:

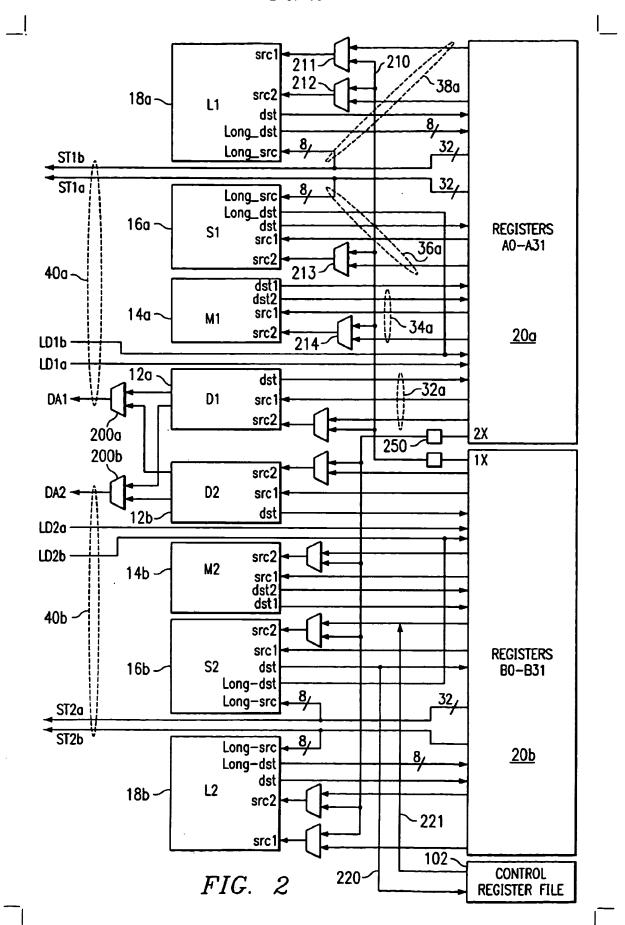
The attached annotated sheets 1, 12, and 14, and corresponding replacement sheets 1 through 10, includes changes to Figures 1, 7C, and 9. The replacement sheets formally present all Figures in this case.

In Figure 1, the block referred to by reference numeral 82, and reference numerals 82, 90, and 100 are canceled.

In Figure 7C, the multiplexer inputs "A", "B", and "C" are canceled from the Figure.

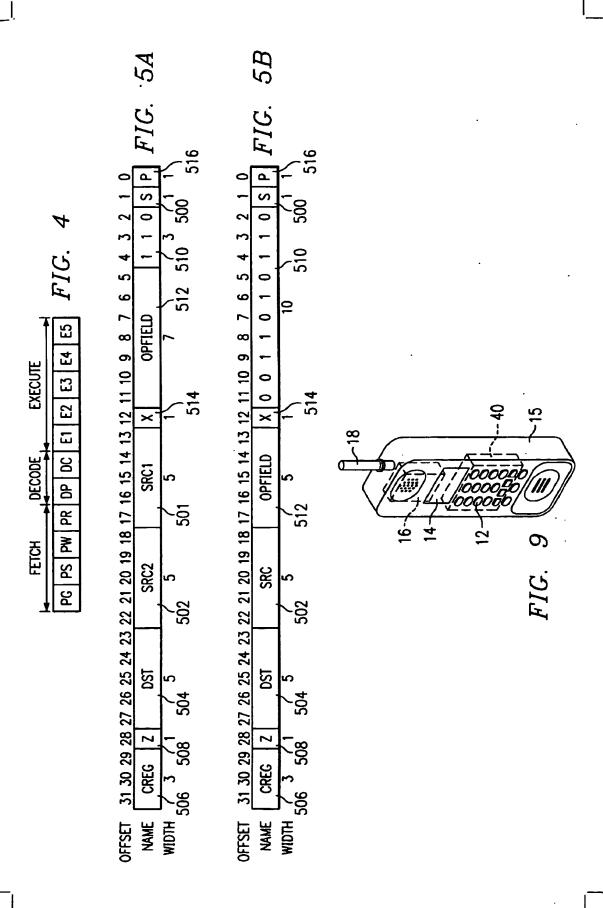
In Figure 9, reference numeral 10/40 is replaced with reference numeral 40.



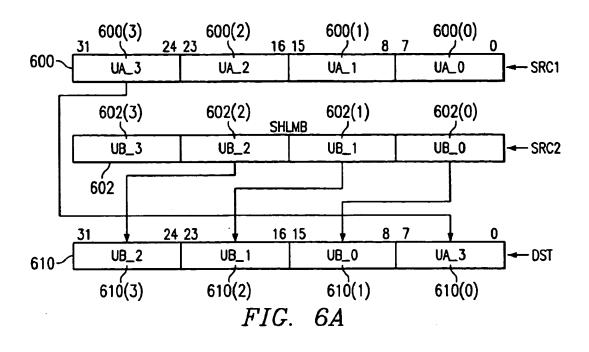


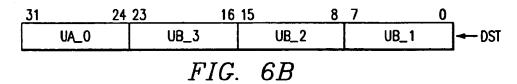
24		3B			3C				3D				3E		
FIC			FIG.			FIG.				FIG.				FIG.	
5 4 3 2 1 0		7 6 5 4 3 2 1 0	000000	ις.		8 0 0 0 0	9		8 7 6 4 3 2 1 0	Y W/ST 1 1 S P	3		9876 43210	R Y LD/ST 0 1 S P	3
13 12 11 SRC1/CST   x		13 12 11	SRC1/CST X	ın		SRC1/CST	5	(E		UCST15	15	UNIT	13 12	OFFSET R/UCSTS MODE	5 4
23 22 18 17 SGR2		الآ 23 22 18 17	SCR2	ഹ	66	53 22 SCR2	S	LOAD/STORE WITH 15-BIT OFFSET (ON THE .D UNIT)	23 22			LOAD/STORE 'BASER' + 'OFFSETR/CST' ON THE .D UNIT	23 22 18 17	BASE R OFF	S
THE .L UNIT	် ေ	THE .M UNIT	DST	က <u>.</u>	THE .D UNIT	DST	သ	N 15-81T OF		DST/SRC	သ	SER' + 'OFFS		DST/SRC	5
OPERATIONS ON THE .L UNIT 31 29 28 27 2; CREG   7 DST	_	OPERATIONS ON THE .M UNIT 31 29 28 27	CREG Z	m	OPERATIONS ON THE .D UNIT	CREG   Z	ĸ	LOAD/STORE WIT	31 29 28 27	CREG Z	٤	LOAD/STORE 'BA	31 29 28 27	CREG Z	3

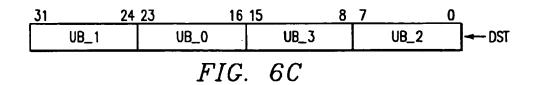
3F			36			3H			3I			31			
FIG.				FIG.			FIG.			FIG.			FIG.		
6 5 4 3 2 1 0	OP 0 0 0 0 S P	9	76543210			8 7 6 5 4 3 2 1 0	0 1 0 S P	2	76543210.	H 1 0 1 0 S P			7 6 5 4 3 2 1 0	0 0 1 0 0 S P	
13 12 11	×					13 12	CSTB	S							
18 17	SRC1/CST.	ß		CST	16	BITFIELD OPERATIONS (IMMEDIATE FORMS) ON THE .S UNIT	CSTA	သ		TSO	16			CST	21
UT 23 22	SRC2	ເດ ့	23 22			EDIATE FORMS)	SRC2	5	UNIT 23 22			E			
N THE .S UNI	DST	လ			5	ATIONS (IMME)	DST	2	ON THE .S .	DST	5	N THE .S UN!	7		
OPERATIONS ON THE .S UNIT	CREG Z	3	ADDK ON THE .S UNIT 31 29 28 27	CREG Z	£	BITFIELD OPERAT	ĮΨ	3	MVK AND MVKH ON THE .S 31 29 28 27	IШ	3	BCOND DISP ON THE .S UNIT	31 29 28 27	CREG Z	3



\_1.



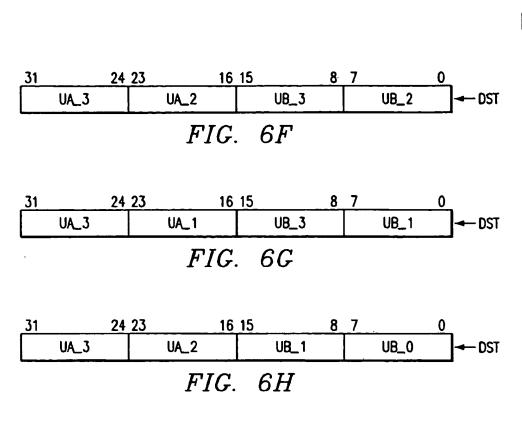


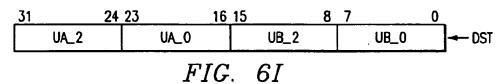


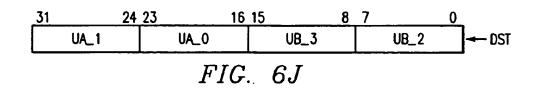
31 24 23 16 15 8 7 0

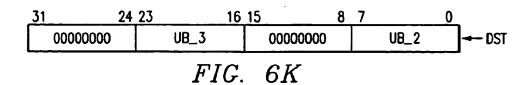
UA\_1 UA\_0 UB\_1 UB\_0 
$$\rightarrow$$
 DST

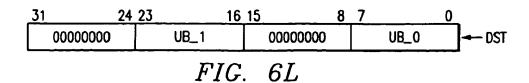
$$FIG. 6E$$

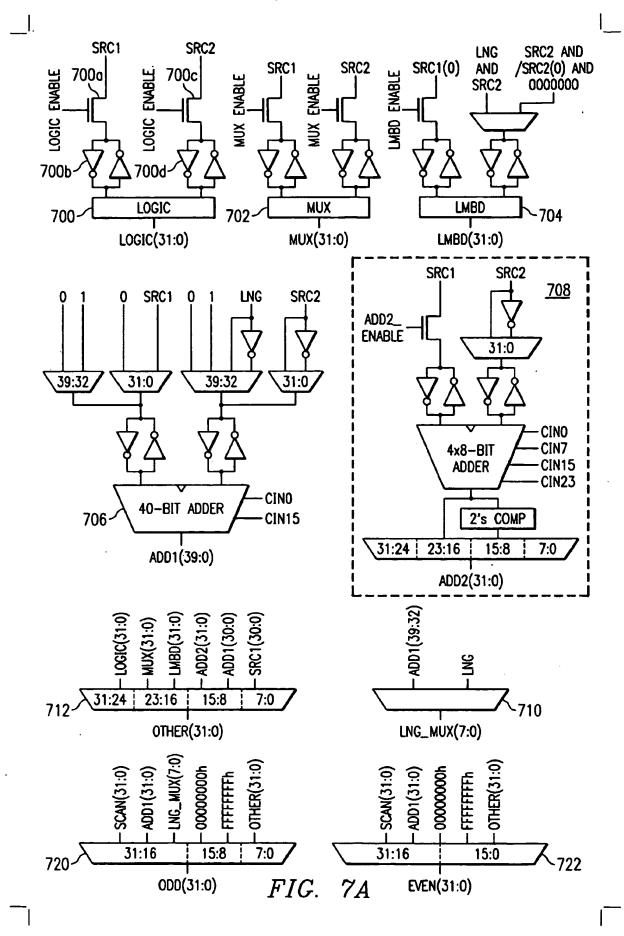


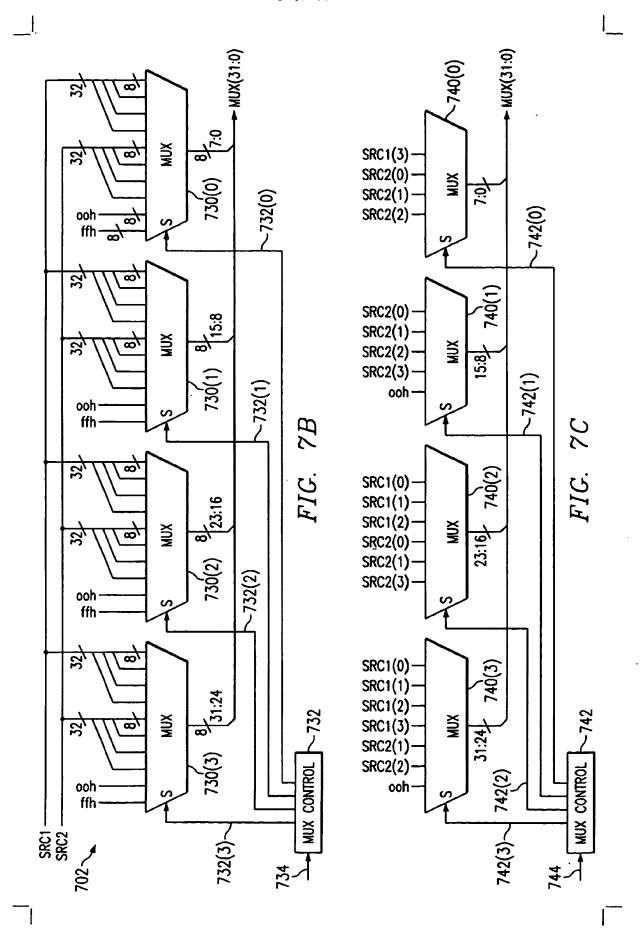












\_I.

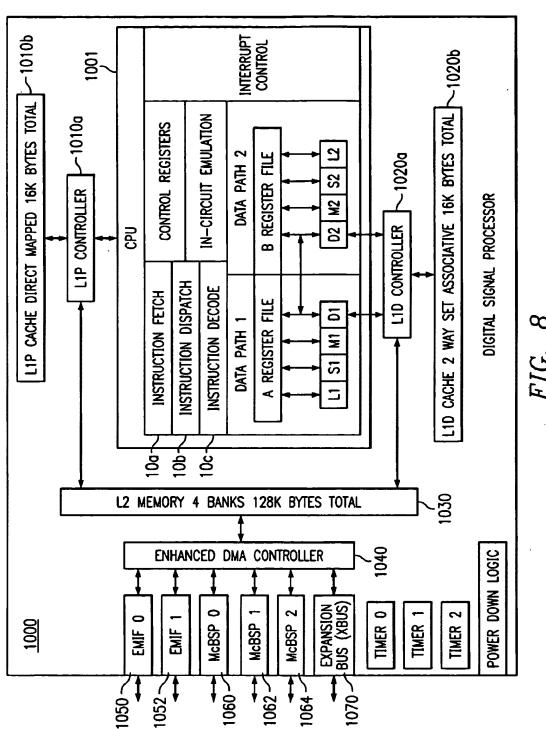


FIG. 8